



Attorney's Docket No.:

07977-093002 / US3164D1

UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Hisashi Ohtani, et al.

Art Unit :

2815

Serial No.:

09/379,702

Examiner:

Eugene Lee

Filed

August 24, 1999

Title

METHOD OF FABRICATING SEMICONDUCTOR DEVICES

Commissioner for Patents Washington, D.C. 20231

TRANSMITTAL LETTER AND PETITION FOR AUTOMATIC EXTENSION

Correspondence relating to this application is enclosed. The required fees are computed below. Please apply any charges not covered, or any credits, to Deposit Account No. 06-1050.

Total Claims

TOTAL FEE DUE

20

43

\$0 \$0

Independent

5

10

0

\$180

Information Disclosure Statement

Applicant hereby petitions under 37 C.F.R. §1.136

for a 1 month extension of time.

\$110

\$290

A check for \$290 is attached.

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Respect fully submitted,

C. Harris

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CERTIFICATE OF MAILING BY FIRST CLASS MAIL

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I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

June 14, 2002

Date of Deposit

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